FIVE ECO_s (Korea)

EV Group announces NanoCleave layer release new technology revolutionizing 3D Integration for advanced packaging and transistor scaling – January 30, 2024

EVG introduced NanoCleave[™], a revolutionary layer release technology for silicon that enables ultra-thin layer stacking for front-end processing, including advanced logic, memory and power device formation, as well as semiconductor advanced packaging. As a result, NanoCleave enables silicon wafer carriers in advanced packaging processes such as FoWLP using mold and reconstituted wafers as well as interposers for 3D SIC. At the same time, its compatibility with high-temperature processes enables completely novel process flows for 3D IC and 3D sequential integration applications − enabling hybrid and fusion bonding even of ultra-thin layers on silicon carriers, thereby revolutionizing 3D and heterogeneous integration as well as material transfer in next-generation scaled transistor designs.



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