semiconductor packaging news

PMT Orders Maskless Lithography System from EVG for Wafer Probe Card Manufacturing – January 25, 2024



News Focused on Semiconductor Packaging

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January 25, 2024

Fan-Out Panel-Level Packaging Hurdles

The semiconductor industry faces significant challenges in fan-out panel-level packaging, as highlighted in a recent news story. These hurdles involve issues related to cost, technology, and scalability, impacting the development of advanced packaging solutions. Semiconductor Engineering



Epoxy for Medical Device Applications Master Bond EP40TCMed epoxy meets the requirements of ISO 10993-5 for noncytotoxicity. It is a thermally conductive and electrically insulative system. Master Bond



VIEWPOINT 2024: Anika Baumhauer, International Strategy & Sales Manager,

SONOTEC GmbH We see continued growth in the semiconductor market. The year 2023 has been tough and challenged us a lot, but we also received a great amount of encouragement and positive feedback from global semiconductor players and research institutes. Increasing material prices, the ongoing uncertainty in the supply chains .

SONOTEC GmbH

How to Select RF Test Socket

Selecting the right RF test socket involves several factors - frequency, insertion loss, return loss, impedance matching, contact force, contact resistance, durability ... Technical Paper

Coal: Don't Burn It, Compute With It

The IEEE Spectrum reports on promising developments in silicon semiconductor alternatives, highlighting the pursuit of more sustainable and efficient materials to replace coal in the semiconductor manufacturing process, potentially reducing environmental impact. **IEEE Spectrum**

Technical Papers

- Highly Selective Epoxy Molding Compound Removal in Advanced Semiconductor Devices
- Motivation for pressure-assisted metallic sintering in Power Electronics
- IR Laser Cleave Technology for 3D Packaging and Logic Scaling
- Next Gen Laser Assisted Bonding (LAB) Technology
- Durability and Cost Benefits Drive Mil-Aero Demand for OCPP
- Revising 5G RF Calibration Procedures For RF IC Production Testing
- Optimizing New Power Switch Technology Using Compound Semiconductors

Targeted Sample Preparation for Deeply **Buried ROI in Advanced Packages** New fault isolation technique using 3D X-ray microscopy &



open a 20 µm wirebond without damage to adjacent wires. ZEISS Microscopy

Buy Ceramic Dispensing Needles Online Ceramic needles with Luer Lok for conductive epoxies and encapsulation. The ceramic molding process allows for smaller more precise inner diameters with a glass like finish. Buy Online. **DL Technology**

Today's Sponsor

MARCH | Nordson Electronics Solutions

Large Capacity Plasma Treatment System - FlexTRAK-SHS 9.6-liter plasma process

chamber handles larger, or more, strips increasing throughput and productivity for electronics/semiconductor packaging.



Test Your Knowledge

If you drop a 5 when rounding a number, you are using the (a) round-to-even rule; (b) significant digit rule; (c) round-off rule; (d) retained digit rule

See answer below.



Press Release

PMT Orders Maskless Lithography System from EVG for Wafer Probe Card Manufacturing

PROTEC MEMS Technology (PMT) has ordered a LITHOSCALE® maskless exposure system from EV Group (EVG), a leading supplier of wafer bonding and lithography equipment .. **EV Group**

Laser Thermal Partners with Angstrom Scientific, Inc. for Sales Representation

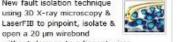
Laser Thermal is pleased to announce the signing of Anostrom Scientific as its newest representative for sales of the company's flagship SSTR-F (Steady State Thermoreflectance .. Laser Thermal

Indium Corporation Expert to Present at **INEMI Seminar Hosted by Zestron Europe**

Indium Corporation Regional Technical Manager and Technologist - Advanced Applications Andreas Karch will present at iNEMI's upcoming seminar, Humidity Robustness and Isolation ... seminar, Humidity Robustness and Isolation ... Indium Corporation

Koh Young Shares Revolutionary Advanced Package Inspection Solutions at the SMTA WLP

Koh Young Technology will be speaking at the SMTA Wafer-Level Packaging Symposium in Burlingame, California on our Multimodal Phase Shift Optics Approach to revolutionize ...





ASML, a leading semiconductor equipment manufacturer, has reported its Q4 and full-year 2023 earnings. The company exceeded expectations, posting strong financial results amidst the global chip shortage, demonstrating its resilience in the semiconductor industry. CNBC

ASML annual profits surge to US\$8.5bn

ASML Holding NV, the Dutch tech giant supplying chipmaking machines, posted a robust annual net profit of 7.8 billion euros (\$8.5 billion), up from 5.6 billion euros, defying China-West trade tensions

Taipei Times

Canada Must Double Down on Chip Industry Talent

Canada is urged to ramp up efforts in nurturing talent within its semiconductor industry as global chip demand continues to soar. Strengthening the nation's tech workforce is essential for its competitiveness in this critical sector.

EE Times



Alphabet cuts ties with Australian AI firm that helped train Bard and Google Search Alphabet, Google's parent company, terminates its contract with Appen, a key player in training its search engine, Bard. This move marks a significant shift in Alphabet's strategy for enhancing its search capabilities. CNRC

Texas Instruments posts disappointing guidance, signaling weakness in chip industry

Texas Instruments' positive sales revenue guidance for the semiconductor industry signals optimism amid global chip shortages. The company's strong performance reflects resilience in the sector, offering hope for continued growth. Digitimes





and see how a digital advertising campaign can deliver results. Semiconductor Packaging News

TSMC Expansion Augurs Well for Chip Industry

TSMC's expansion plans hold promise for the AI chip supply chain. The semiconductor giant's growth strategy is poised to meet the rising demand for AI-related technologies, ensuring a steady and robust supply. **EPS News**

Nvidia CEO's low-key China visit seen as a goodwill gesture towards key market as US chip firm grapples with sanction issues

NVIDIA CEO's recent visit to China is viewed as a goodwill gesture toward a crucial market. The US chip glant seeks to strengthen ties amidst challenges in the tech war. South China Morning Post

Koh Young America, Inc.

Particle-Free Plasma Oxide Reduction Ar/H2 plasma for flux-free flip chip bonding. Low temp, Surfx uniform 100 mm wide beams. Fast oxide removal. No damage or particles. Contact us for integration into your TCB bonder. Surfx Technologies

Quote of the Day

You only have to do a very few things right in your life so long as you don't do too many things wrong." Warren Buffett

Die Attach Material Comparisons Packaging high-power GaN devices? This study compares CuW and CMC bases with H20E and AuSn, revealing 34.5°C cooler junctions with CMC/AuSn. Revolutionize GaN efficiency/reliability. Stratedge Corporation

2024 IEEE 74th ECTC Conference The ECTC conference is the

premier international event ECIC that brings together the best in packaging, components & microelectronic systems science, technology & education. May 28-31. FCTC

The day was Jan 25. What year was it? World's Largest Diamond Found

At the Premier Mine in Pretoria, South Africa, a 3106-carat diamond is discovered during a routine inspection. It was the largest diamond ever found. See the answer below.



Plasma Etch

Thin Is In This article discusses market

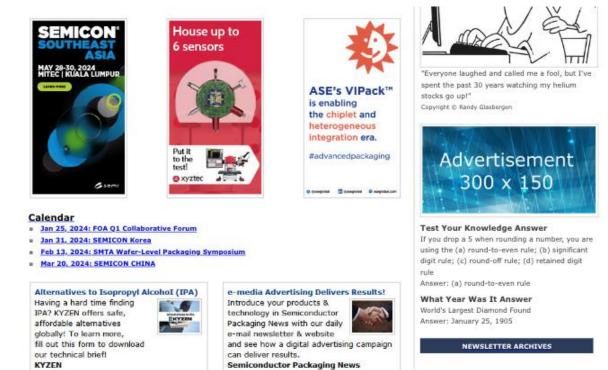
drivers & technical challenges related to handling & working with ultra-thin devices. Being able to robustly and safely remove thin die from tape is a key requirement. **Royce Instruments**











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Jeff Ferry, Publisher | Ken Cavallaro, Editor/Business Manager

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