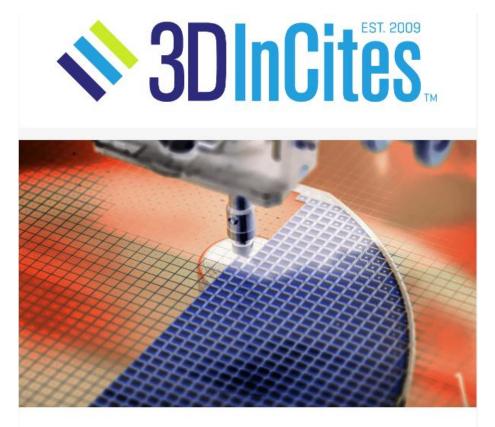


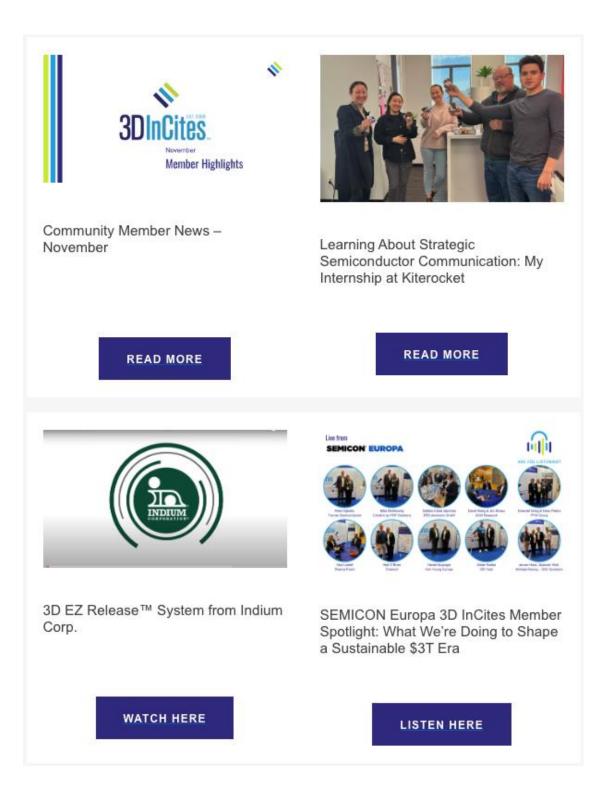
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Flux-less TCB for Fine-Pitch Applications and Its Extension to Cu-Cu TCB

Typical interconnects are composed of solder-capped copper pillars, which exist either on a chip, on a substrate, or in some cases on both sides. The finest pitches in more recent advanced packaging applications range from 25 – 80µm and are implemented either through a conventional mass reflow (MR) or via a more sophisticated thermal compression bonding (TCB) process. The pitch and dimension scaling below 50µm, though very enticing, comes with challenges that cannot always be surmounted using traditional flux-based processes.

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