semiconductor packaging news

EV Group Hybrid Bonding and NIL Solutions Highlighted at SEMICON Taiwan – September 1, 2023

semiconductor packaging news

We search for industry news, so you don't need to.

Introducing ASE's Integrated Design Ecosystem™ (IDE)

ASE's IDE is a collaborative design toolset optimized to systematically boost advanced package



architecture across its VIPack™ platform.

ASE

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September 1, 2023

Need To Share Data Widens In IC Manufacturing

The demand for data sharing in IC manufacturing is expanding rapidly. As technology advances, collaboration and information exchange between various stakeholders become increasingly crucial. Manufacturers, designers, and foundries must seamlessly share data to ensure efficient production processes, quicker problem resolution, .

Semiconductor Engineering

System - FlexTRAK-SHS

chamber handles larger, or

throughput and productivity

9.6-liter plasma process

more, strips increasing

Large Capacity Plasma Treatment

for electronics/semiconductor packaging.

MARCH | Nordson Electronics Solutions

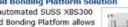


SUMMARY

BGA Component Rework Simplified

When you're confronted with tough BGA rework, who do you turn to? Discover the #1 resource used by military contractors. We have solutions for all your BGA rework and repair needs.

Circuit Technology Center



for state-of-the-art hybrid bonding at the highest accuracy on 200 and 300mm substrates SUSS MicroTec



The worldwide smartphone market has witnessed remarkable growth, as global shipments reached a new high in ...

IDC

Decade While iOS Share Climbs to Record Levels in 2023

The choice of packaging technology is influenced by the communication interface

Worldwide Smartphone Shipments Forecast to Reach Lowest Volume in a

Heterogeneous IC Packaging: Optimizing Performance And Cost

between chiplets.

Technical Paper

Performance & Efficiency Cores For Servers

A new approach to server efficiency and performance emerges as researchers

Semiconductor Engineering

focus on creating specialized cores ...

Technical Papers

- 3D Sensing Solutions
- Critical Cleaning Requirements to Overcome Advanced Packaging and Defluxing Challenges
- Reliably Reinforce BGAs While Minimizing Stress on Solder Joints
- Series A: Electrical Insulation of Al2O3-Coated Bonding Wire
- Production Testing Of Discrete Power Products
- Essential Inspection Requirements in the Era of Convergence
- Photosensitive Permanent Bonding Material Designed for Polymer/Metal Hybrid Bonding **Applications**

Microelectronics Assembly & Packaging

Flip Chip, Thermocompression, Wire Bonding, Encapsulation. Applications: RF, mmWave, Military, LiDAR, Medical, Photonics, MEMS.



SAWA SC-BM500E Automatic Stencil Cleaner

Ultrasonic combined with spray cleaning provides an excellent solution for all kinds of applications including wafer bump and electroform stencils. Seika



DOWNLOAD

SUMMARY

Today's Sponsor



What line on a map connects all points of the same elevation?

See answer below.

Hybrid Bonding Platform Solution

The automated SUSS XBS300 Hybrid Bonding Platform allows



EV Group Hybrid Bonding and NIL Solutions Highlighted at SEMICON Taiwan

EV Group (EVG) announced that new developments in 3D/heterogeneous integration and augmented reality (AR) waveguide manufacturing enabled by its advanced wafer ... EV Group

solutions at SEMICON Taiwan 2023

Brewer Science, Inc., will present new developments in temporary and permanent bonding material technologies for advanced packaging this week at Asia's two premier ...

Brewer Science, Inc

Vietnam Business Summit 2023: Paving the Way for Vietnam's Semiconductor Industry

Growth

Key semiconductor industry players and stakeholders including partners, government agencies, and academia will gather September 28-29 in Hanoi for the Vietnam Business ...

From Silicon to Sustainability!

Electronics Goes Green 2024+, the world's premier event on sustainability & electronics, takes place in Berlin in June 2024. Abstract submission extended to Dec 6.



Fraunhofer IZM

AmTECH Microelectronics

EUV pod supplier alliance welcomed by key customer

Gudeng Precision Industrial Co, a key supplier of extreme ultraviolet (EUV) pods to Taiwan Semiconductor Manufacturing ...

Taipei Times

Nvidia partner expects sales to double Quanta Cloud Technology (QCT), Nvidia Corp's partner, projects a doubling of sales

I believe that freedom is the deepest need of every human soul. George W. Bush

Quote of the Day

SUMMARY

for its AT servers **Taipei Times**

China exports of gallium and germanium surged before restrictions took effect in August, report shows

SUMMARY

China's exports of gallium and germanium experienced a significant surge as new restrictions came into effect .

South China Morning Post







Apple reportedly tests 3D printing to manufacture the new Apple Watch

SUMMARY

Apple is reportedly exploring the integration of 3D printing technology into its manufacturing process for the ...

Dell, HP and Apple supplier Foxconn apply to make laptops in India under SUMMARY US\$2.1 billion incentive plan

Major tech companies Dell, HP, and Apple supplier Foxconn are set to manufacture laptops in India

South China Morning Post

Huawei's new phone juices Chinese chip stocks

SUMMARY

SUMMARY

Huawei's latest smartphone launch has ignited a surge in Chinese chip stocks. The tech giant's new ...

Asia Times

Automate Your Hi-Mix Die Attach

MAT 6200 Crossover, low cost, entry level auto Die-Attach ideal for low volume Prototypes, NPI, CMS

environments. Easy GUI Set-up, Rapid turnover, Multi-Process support.

MicroAssembly Technologies

Automatic Grading Without Assistance

Eliminate the need for operators to assess bond testing failure modes at the end of an automation run.

Auto grading uses machine vision software to process the test result images. xyztec

China grants first generative AI model for download to play catchup

China introduces "Ernie Bot," a cutting-edge generative AI software powered by big data. Ernie Bot's launch ...

Digitimes







Fusion and Hybrid Bonding Experts EVG's industry-leading process solutions, expertise and HI Competence Center accelerate the development and implementation of heterogeneous

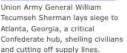
integration technology. **EV Group**

Essential Inspection Requirements in the Era of Convergence

Herein, learn ways to optimize processes and boost quality as advanced packages and board assembly converge. Read more.

Koh Young America, Inc.

What Year Was It? Atlanta Falls to Union Forces





The day was Sep 1. What year was it?

Improved Wire Bond Reliability

Auxiliary wires are commonly defined as an additional wire outside of a normal ball-tostitch wire bond. The

motivation behind using auxiliary wires is the inherent weakness of the stitch bond. Palomar Technologies, Inc.

Alternatives to Isopropyl AlcohoI (IPA)

Having a hard time finding IPA? KYZEN offers safe, affordable alternatives globally! To learn more, fill out this form to download our technical brief!



KYZEN

Cartoon of the Day



"I'd like to apologize for asking you to step outside of your comfort zone." Copyright © Randy Glasbergen







Calendar

- Sep 5, 2023: Advanced Packaging Summit 2023
- Sep 6, 2023: SEMICON Taiwan 2023
- Sep 6, 2023: Overview of semiconductor manufacturing for European Attendees
- Sep 7, 2023: EH&S Japan TC Chapter Meeting

Automate Your Hi-Mix Die Attach

MAT 6200 Crossover, low cost, entry level auto Die-Attach ideal for low volume Prototypes, NPI, CMS environments. Easy GUI Set-up, Rapid turnover, Multi-Process support.

MicroAssembly Technologies

Automatic Grading Without Assistance

Eliminate the need for operators to assess bond testing failure modes at the end of an automation run.

Auto grading uses machine vision software to process the test result images. **xyztec**

3D chip stacking, hybridization services

ARC provides: • turnkey, 3D Chip Stacking & Hybridization Services. • Wafer Bumping to Completed Chip Stack at ARC.



 Wire bonding of die stacks, all in-house. • ISO|ITAR.

Test Your Knowledge Answer

What line on a map connects all points of the same elevation? Answer: A contour line

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