

EVG announces NanoCleave™ layer release technology revolutionizing advanced semiconductor process – September 25, 2022

EVG introduced NanoCleave™, a revolutionary layer release technology for silicon that enables ultra-thin layer stacking for front-end processing, including advanced logic, memory and power device formation, as well as semiconductor advanced packaging. NanoCleave is a fully front-end-compatible layer release technology that features an infrared (IR) laser that can pass through silicon, which is transparent to the IR laser wavelength. As a result, NanoCleave enables silicon wafer carriers in advanced packaging processes such as Fan-out Wafer-level Packaging (FoWLP) using mold and reconstituted wafers as well as interposers for 3D Stacked ICs (3D SIC). "NanoCleave will help enable our customers to realize their advanced device and packaging roadmaps through a highly versatile and universal layer release technology that works with standard silicon wafers and wafer processes − enabling seamless integration in the fab and saving our customers both time and money." stated Paul Lindner, executive technology director at EV Group.

