



EV Group and Teramount announce collaboration to implement innovative packaging technologies for photonic integrated circuits – March 2, 2022



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Ferroelectric devices forge ahead

At IEDM 2021 the new star of the emerging memory firmament was ferroelectric memories, either in 1T/1C (FeRAM) or 1T (FeFET) structures. [More>>](#)



Tech News

[Silvaco announces that Teledyne e2v expands adoption of its Victory TCAD solution to further accelerate development of CMOS image sensors](#)

Silvaco Group, Inc., a provider of TCAD, EDA software, and Design IP, today announced that Teledyne e2v, a global leader in imaging solutions, has renewed its commitment to Silvaco's Victory TCAD solution to accelerate its CMOS image sensor development. [More>>](#)



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Featured Video

Fit-Line's Nexus Connect insert fittings ensure safe, reliable, leak-free connections and are a strong choice for use in high-purity and corrosive chemical semiconductor applications. **George Alvarado, Vice President/General Manager at Fit-Line**, talks to Editor-in-Chief Pete Singer at SEMICON West.



Business News

Leaders in semiconductors, packaging, IP suppliers, foundries and cloud services join forces to standardize chiplet ecosystem

Advanced Semiconductor Engineering, Inc. (ASE), AMD, Arm, Google Cloud, Intel Corporation, Meta, Microsoft Corporation, Qualcomm Incorporated, Samsung, and Taiwan Semiconductor Manufacturing Company today announced the formation of an industry consortium that will establish a die-to-die interconnect standard and foster an open chiplet ecosystem. [More>>](#)

EV Group and Teramount announce collaboration to implement innovative packaging technologies for photonic integrated circuits

The collaboration will leverage EVG's nanoimprint lithography (NIL) technology, expertise and services with Teramount's PhotonicPlug technology. [More>>](#)

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STMicroelectronics has been named Top 100 Global Innovator 2022. [More>>](#)

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